FIG. 1

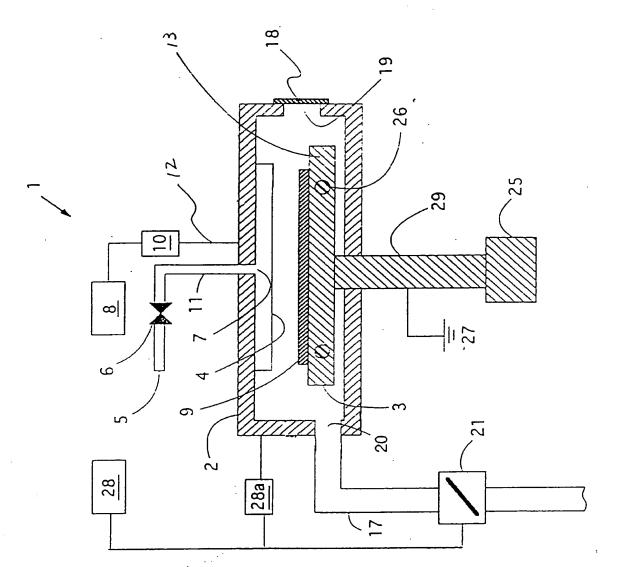


FIG. 2

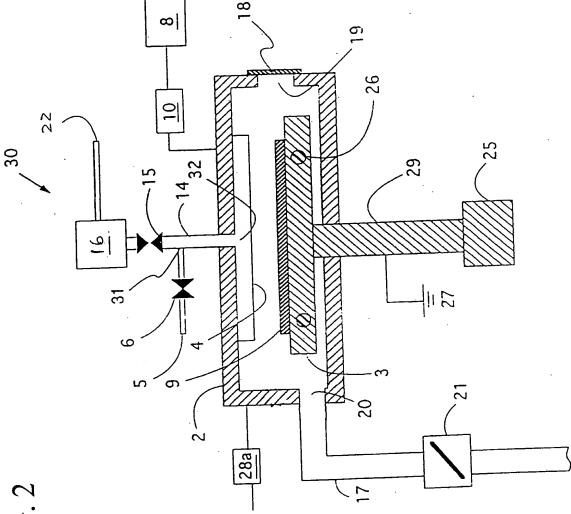
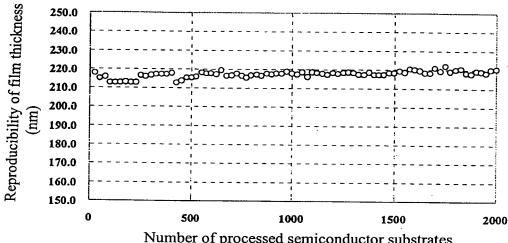


FIG. 3



Number of processed semiconductor substrates



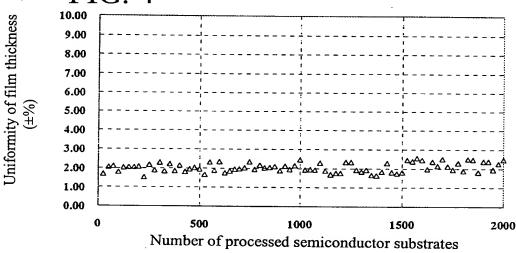
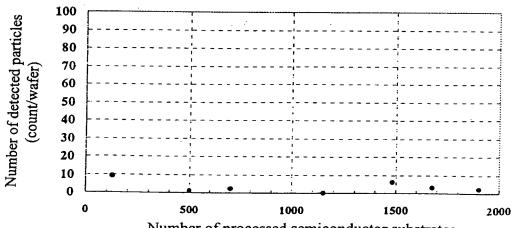


FIG. 5



Number of processed semiconductor substrates